

**Amendments to the Specification:**

Please replace the paragraph 17 beginning at page 9, line 14, with the following rewritten paragraph:

Adjacent to the controlled capacitance bridges, pairs of trim traces are disposed to provide a trimable, or adjustable, capacitance. In the embodiment shown, a first pair of trim traces 120 and a second pair of trim traces 121 are disposed at the input 105 and the output 115 of the coupler, respectively. Trim traces 120, 121, consisting of parallel copper traces 215, 220, 151 and ~~121~~ 158 of microstrip conductors 105 and 122, provide an additional capacitance. This additional capacitance is typically used to adjust the performance of microstrip coupler 100 in addition to the capacitance provided by controlled capacitance bridges 140, 141. In particular, the capacitances provided by the pairs of trim traces 120, 121 typically affect coupler directivity. The trim traces 120, 121 are shown as parallel conductors disposed on the substrate 101 and separated by a gap (s). In the exemplary embodiment gap 's' is 0.010 in. Each pair of trim traces 120, 121 provides capacitance inversely proportional to the spacing between conductors and proportional to their length as is known to those skilled in the art. In one exemplary embodiment, the capacitively coupled length 'l' is 0.151 in.